1. **Concept:** Superposition and Phasor Analysis  
   **Find:** $i_o$ in the circuit below using superposition  
   **Check:** Result should have one part that is roughly $-70^\circ$

![Circuit Diagram 1](image1.png)

2. **Concept:** Source Transforms and Phasor Analysis  
   **Find:** Use Source Transforms to find $i$ in the circuit below.  
   **Check:** Result should have a magnitude close to 9

![Circuit Diagram 2](image2.png)